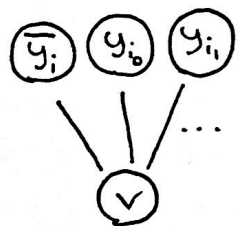


Thm \overline{W} is analytic

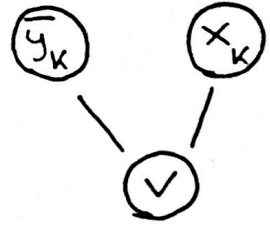
Pr: Assume \mathbb{N}^* is ordered by ω , i.e.

$\mathbb{N}^* = \{t_0, t_1, \dots\}$ and also assume $t_0 = e$ (empty node)

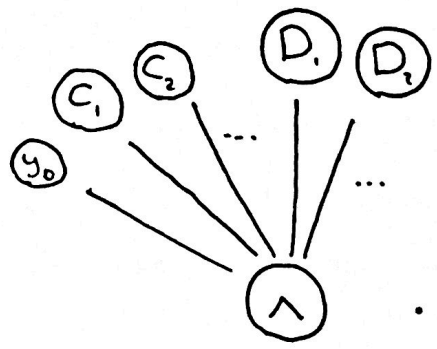
$\forall i \in \mathbb{N}^+$ we define (C_i) - V_i -circuit of the

form:  where i_j is an index of j -th successor of t_i .

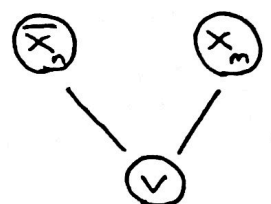
For every $k \in \mathbb{N}$ we define (D_k) - V_i -circuit

of the form: 

The circuit which accepts \overline{W} is the following:



We should also add

circuits of the form  for all pairs

n, m such that t_m is a predecessor of t_n .
(to make sure that we are accepting trees and nothing else)